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ternary content addressable memory coupled to arithmetic logic unit within the central processing unit configured to perform one or more matching operations

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1. [A ternary content addressable search engine - Solid-State Circuits ...](#)

and the central processing unit (CPU). One solution to this lines, it is possible to perform match operations on arbitrary wide words. ...

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2. [IEEE 100 The Authoritative Dictionary of IEEE Standards Terms](#)

or through one or more coupler interfaces, to perform all of a central processing unit within a single LSI circuit. Note: ...

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3. [APPARATUS AND METHOD FOR HIGH THROUGHPUT NETWORK SECURITY SYSTEMS ...](#)

Each of the one or more processing channels may be configured to use the ... one of the multitude of processing cores includes an arithmetic logic unit. ...

[www.freepatentsonline.com/20080077793.html](#) · [Similar patents](#)

by T Fan - 2008

4. [Dynamic unknown L2 flooding control with MAC limits - Patent 7149214](#)

Dec 12, 2006 ... 5909686, Hardware-assisted central processing unit access to a forwarding Often intermediate nodes employ a content-addressable memory ...

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5. [\(WO/2007/002466\) ACCESS CONTROL LIST PROCESSOR](#)

and forwards the search key to a ternary content addressable memory 105 (TCAM), two arithmetic-logic units 41215 4122 (ALU1 and ALU2) each coupled to ...

[www.wipo.int/pctdb/en/wo.jsp?IA=US20060245768&DISPLAY=DESC - 65k](#)

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6. [\(WO/2008/073824\) DYNAMIC PROGRAMMABLE INTELLIGENT SEARCH MEMORY](#)

The PRISM memory also optionally provides ternary content addressable memory The PRISM controller, block 1203, coupled with the content stream logic, ...

[www.wipo.int/pctdb/en/wo.jsp?WO=2008073824&IA=](#)

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15. Digital Circuit Design

PREFACE Digital systems are created to **perform data processing** and control tasks Basic Computer Architecture 59 microprocessor **Arithmetic Logic Unit** ...
www.scribd.com/doc/4291255/Digital-Circuit-Design · 97% · Cached · Similar pages

16. Tamil Technical Dictionary

Send to (Required, with one email address per line) **arithmetic logic unit** - தமிழ்
¾ Á½ « ð , jñ ; ALU arithmetic operation - தமிழ்க் கலை அளவீடு மூலம் ...
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17. /harddr/mig/a339385.tif

storage and bit-serial processing of one to three **ternary** contiguous bit fields
imaging unit has been configured for. 323-002 (neo02.wp5/net)|Issue 2, ...
www.stormingmedia.us/58/58339/A583393.pdf · Similar pages
by Rad Lee · 1995 · Reader articles · All 2 versions

18. Webster's Online Dictionary - The Rosetta Edition

DRAM CONTENT ADDRESSABLE MEMORY USING PART OF THE CONTENT AS AN ADDRESS · DRAM CONTROL CIRCUIT DRAM WITH INTEGRAL SRAM AND ARITHMETIC-LOGIC UNITS ...

www.websters-dictionary-online.org/browse/inventions/index.htm · 328k · Cached · Similar pages

19. MCLEAN, VA, US - Patents

20090002882 **DISK DRIVE UNIT** - According to one embodiment a disk device
KEY SELECTION DEVICE AND PROCESS FOR CONTENT-ADDRESSABLE MEMORY 10-23-2008 ...
www.uspto.gov/patents/city/us_va_mclean · 178k · Cached · Similar pages

20. ARLINGTON, VA, US - Patents

20090006934 Triggering diagnostic operations within a data processing
20080263115 Very long **arithmetic logic unit** for security processor 10-23-2008 ...
www.uspto.gov/patents/city/us_va_arlington · 437k · Cached · Similar pages

21. [PDF] "Who controls the vocabulary , controls the knowledge."

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Central Processing Unit (see CU, Dual-core Chip) **Ternary Content Addressable Memory**, TCE, Transaction Cost Economics ...
[www.fbc-econferenceresources.economyministries.org/_d001_e.pdf](http://fbc-econferenceresources.economyministries.org/_d001_e.pdf) · Similar pages

22. Scientific Commons: Senior Member

A **Ternary Content-Addressable Memory** (TCAM) Based on 4T Static Storage (2003) Novel radix- modulo- arithmetic units for residue number system ...
en.scientificcommons.org/senior_member · Similar pages

23. [PDF] Deploying Concurrent Applications on Heterogeneous Multiprocessors

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http://www.google.com/search?hl=en&qj=tamary+content+addressable+memory+search+dns_qj=tl&sa=X&ei=mp&ct=&oi=doct&sa=t&sqaf=images (3 of 13) [16/2009 6:48:22 PM]

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accuracy within one bit of the encoder resolution (10°). The FPGA based encoder processing unit performs preprocessing, filtering and post pro ...

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32. [PDF] Algorithm-Architecture Trade-offs in Network Processor Design

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read from memory. Then, the arithmetic operations can be performed. Last, 41 shows a minimal computing system with a central processing unit ...

http://fp.tkk.ee.ehu.ee/~elba/cvpub/people/fleis/paperlist_gres.pdf · [Similar pages](#)
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33. Telecommunications Glossary 2000

memory: 1. All of the **addressable** storage space in a **processing unit** and other

One or more transmitters that are capable of **operation** while in motion. ...

www.its.bldrdoc.gov/projcsidc/glossary/000012.html · [Similar pages](#)

34. exclamation mark " double quote % percent , comma 0 zero 0/1 ...

(2001-01-27) **Arithmetic and Logic Unit** processor (ALU or "mill") The part of the central processing unit which performs **operations** such as addition, ...

www.jjj.de/milstone/GICTym.html · [Similar pages](#)

35. Projects by Community

Uses BigDecimal to handle basic **arithmetic** and **ternary** expressions. ... Libraries and programs to support unit and functional testing of Java UIs. ...

community.java.net/projects/community/javabots · 422 · [Cited](#) · [Similar pages](#)

36. [PDF] COMPUTER BASICS AND COMPUTER SYSTEMS

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If more than one file is compared using the same criteria by **matching**, **central processing unit** (CPU) and as the backbone of the computer, ...

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37. Tuesday.html

A description of **memory operation** and various design details will be described.

2MByte Flash Memory, 256KByte fast SRAM and a 16-bit MAC unit. ...

www.cs.louisville.edu/~markware/mvc38/PROGRAM/Tuesday.html · 103k · [Cited](#) · [Similar pages](#)

38. PhD Thesis Links

In many practical situations, **one or more** desired signals need to be recovered from **arithmetic operations**, tests (or branches) and **memory operations**. ...

www.kreinik.dtu.dk/~gkthesis/view.php?orderby=Author&Author=8708 · [Cited](#) · [Similar pages](#)

39. [PDF] Reference Documentation

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best practices for unit testing). The Spring team have found that the correct that **one can perform some operations** with ADO.NET within a consistent ...

springframework.net/docs/1.1.2/reference/pdf/spring-net-reference.pdf · [Similar pages](#)

evaluating unit is configured such that, by using a predetermined ...

www.patentoffice.nic.in/pnp/patent/journal_archive/journal_2006/pat_arch_062006/

official_journal_06062006.pdf - [Similar pages](#)

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49. BibTeX bibliography ipardfcomp.bib

There was **one** volume per year, with **%%** **one** to six issues, through 1989. digital

... Parallel Processing, content addressable memory ...

www.math.utah.edu/pub/bibTeX/tpdb/istcomp.html · [Similar pages](#)

50. [PDF] ICECS 2004 The 11th IEEE International Conference on Electronics ...

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of logic elements and hundreds of embedded multipliers and **memory units**.

matching to **address** the challenges of designs at RT and higher abstraction ...

www.ee.bgu.ac.il/~koren4/Conference_Guides_and_Abstracts/Book-3.pdf

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51. DoD SBIR FY04.1 - SOLICITATION SELECTIONS w/ ABSTRACTS

In addition, **one or more** new, advanced algorithms for retrieving detailed for a large integer processor (LIP) able to **perform arithmetic operations** ...

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52. [PDF] First Alloy Workshop

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one or more compositions infinitely many times. SERA's implementation, the

operations that a **unit** may **perform** during execution affect its syn ...

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53. [PDF] Topics in Software Design Volume 1

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by **one or more** operation. Figure 6.5 shows that the **operation** stretch k can be

machine would not be **configured** properly. Thus, **unit** testing is very ...

[www.cas.mcmaster.ca/csl/papers/SQRL/report34.pdf](http://cas.mcmaster.ca/csl/papers/SQRL/report34.pdf) · [Similar pages](#)

54. Stay Hungry, Stay Foolish - Blog

This means **one more** pointer indirection for every index **operation** in the array,

One benefit of **unit** testing is that it helps you develop your code ...

algorithm.com/authors/files/category/2026on-coding.php · [401k](#)

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55. [PDF] Information Technology Security Evaluation Manual (ITSEM) Version 1.0

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The recommended method for doing this is to **perform one or more** visits to

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56. [PDF] Rule-based Compilation

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The data-parallel programming model has been **one of the more successful** ap

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time performance penalties, the compilation system must perform within ...

[www.pdfs.semanticsweb.org/pubs/pdf_dibressant.pdf](#) · [Similar pages](#)

57. [\[PDF\] 3rd Workshop on Ontologies in Agent Systems](#)

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Each quantity belongs to exactly **one** dimension. A **unit** is select and **perform operations** without violating the consistency ...

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by S Melchiorre · All 3 versions

58. [\[PDF\] The PowerPC Compiler Writer's Guide](#)

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The 32-bit register whose bits reflect the outcome of certain fixed- point **operations**.

Fixed-Point Unit. A logic block that executes integer **arithmetic** and ...

[or.yo.tu2005-590/powerpc-cwg.pdf](#) · [Similar pages](#)

59. [\[PDF\] 10th September 1993 Version 1.0 Page 261 ITSEM PART](#)

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There is a basic crypto **unit** which can be **configured** to be a HCU or a TCU is either 0 or 1) rather than **ternary logic** (where the output of the gate ...

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60. [View All Technical Reports: UNM Computer Science](#)

The aggregation can be a simple **arithmetic operation** or a more complex sector graph model vertices are placed uniformly at random in the **unit** square ...

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· Cached · [Similar pages](#)

61. [RubyBook](#)

More file operations The File class also supports a number of other file **UNIT**

TESTING Test Driven Development is the new hotness, especially in the ...

[www.docstoc.com/docs/1395691/RubyBook](#) · 282k · [Cached](#) · [Similar pages](#)

62. [\[PDF\] SEMI International Standards: Compilation of Terms](#)

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components that together **perform one or more** functions is represented by a single uniquely **addressable unit** of information **within** the document; ...

[www.semi.org/semi/groups/public/documents/web_content/P007083.pdf](#)

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63. [\[PDF\] ABSTRACT Title of Dissertation: TOWARDS THE SEMANTIC WEB ...](#)

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licitly commit to **one or more** autonomously developed ontologies. This technique permits quence of **operations** are treated as a **unit** ...

[www.cs.tugraz.at/~heinrich/pubs/towards-the-semantics.pdf](#) · [Similar pages](#)

64. [Technical Resources](#)

When you start writing **unit** tests, you try to make your classes **more unit** of six units of study (Boolean Functions and Computer Arithmetic; Logic; ...

[www.google.com/coop/cse/groups/119217016303753065641/6GDfjyQzOuDrWtE/](#)

[http://www.google.com/search?hl=en&q=ternary+content+addressable+memory+search+has_q&tbo=us+q=tcs+mp3+for+dts+iso+docs+th+&sa=I#sa=I](#)

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65. [PDF] SCALABLE COMPUTING Practice and Experience

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GRIS executes (and creates the processes for) **one or more** affected the unit of computation. It is a sequential flow of control **within** a single ...

www.cs.cmu.edu/afs/cs/CAF/E_6_1.pdf · [Similar pages](#)

66. Cover Pages: XML Articles and Papers. January - March 2002.

Of course, a message could include **one or more** documents. Within a near decomposable **unit**, it is important to reduce complexity in data sharing. ...

xml.coverpages.org/xmlPapers2002.html · 835k · [Cached](#) · [Similar pages](#)

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to extract **content** and perform automated **processing** on the documents. are used to ensure that a sequence of **operations** are treated as a **unit**. For ...

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68. [PDF] Technical Question Bank

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between the **main memory** and the CPU (center processing unit). runtime database record checkpoint as **one** where **one or more** matching records were found ...

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4000_questions.pdf · [Similar pages](#)

69. [PDF] Teradata RDBMS SQL Reference Volume 2 - Statement and Transaction ...

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by **one or more** AMPs in order to **perform** a task in response to a request parcel.

Loosely defined, a query block is a **unit** for which the Optimizer ...

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70. [PDF] Algebraic Process Calculi: The First Twenty Five Years and Beyond ...

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each action occurring **within** a node takes **one time unit**. Blocks of FPGA logic cells will be allocated and **configured** to **perform** the functional ...

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71. [PDF] epitiib.pmd

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Includes **content addressable memory**. **one or more** loads. Covers, for example, switch control ... Power supply unit operation within a portable ...

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72. [PDF] The Unified Modeling Language Reference Manual

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and **one or more** actors of the system **unit**. The purpose of a use case is to

perform operations. It is similar to an object diagram that shows the ...

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73. [PDF] Volume 32 Number 1 April 2008

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arithmetic and **address operator** have been removed). On the producer side of the prototype are connected to a video processing unit to extract high- ...

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74. false false false Bag false false false false false false ...

This class represents a generic and instantiable **unit of work**, It serves as an aggregation point to associate **one or more** of the following elements: ...

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75. [PDF] LAC2005 Proceedings

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or parallel, and similar operations that involve the spawning of unit generators.

gramming; it compares the content of a memory address ...

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76. [PDF] Mathematical Journals as Reasoning Agents: Literature Review Contents

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Once constructed, the user can do various **operations within** this ring. There are

ways to use **more than one** ring at once. Use $S := Z[5][a,b,c]$, $F := a - b$; ...

[www.risc.univ-linz.ac.at/publications/download/risc_3442/Math Agents for SFB-2010-03-10-12909.pdf](#) · [Similar pages](#)

77. [XLS] Cart 1

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13.3.1 Arithmetic Logic Unit (ALU), 13.3.2 Register File, 13.3.3 Control Unit ...

15.1.1 Central Processing Unit, 15.1.2 Memory, 15.1.3 Input/Output Ports ...

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78. [PDF] Mr. Neighborly's Humble Little Ruby Book

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method allows you to push **one or more** elements onto the end of an array; be

sure to check out the Test::Unit documentation at <http://www.ruby-> ...

[www.humblelittlebook.com/book/mr.pdf](#) · [Similar pages](#)

79. [PDF] Undergraduate Prospectus 2006-2007

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instructions, **Arithmetic and Logic Unit**, Basic ... machine level: **Central Processing Unit (CPU)**, **memory**, and Input/Output: Interfacing CPU ...

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carry out the design technology research in the context of **one or more real** entire

program into a single compilation unit. To manage compile time, ...

www.gigascale.org/pubs/reports/annual/AnnualReportFinal2000.pdf · [Similar pages](#)

81. [PDF] EBU Tech 3274-2 - 1998 EBU Glossary, Part3

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CPU, microprocessor/**central processing unit**. Central component of a computer, A stream of all the digital data carrying **one or more services** within a ...

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82. [PDF] EPI FLY PART 2.pmd

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one or more leads. Covers, for example, switch control, to distribute power where required. ... This code covers **operation** of a power supply **unit** within ...

scientific.thomsonreuters.com/pdf/eipj/manualcodes2.pdf · [Similar pages](#)

83. Haskell Communities and Activities Report

Nov 5, 2008 ... Therefore, the performance of **arithmetic operations** is We are a two-man **unit** of functional programming research **within** the ...

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84. [PDF] Laboratory Directed Research and Development Program

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Memory (PIM) design for the **central processing units**. (CPU). One of our first projects was the evaluation of two, related processors that are designed to ...

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85. [PDF] Brain-Computer Interfaces

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single processing unit, its task can be parallelized by executing the detection the **operations** are listed in the order of the number of **arithmetic** ...

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86. [PDF] MANDATORY DISCLOSURE

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Unit operations (symbols) and **unit processes**. Batch and continuous **processing**, only and **content addressable memory**. RAM - Static & dynamic. ...

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furthermore be tuned by changing **one or more** of the anisotropy field, the **emagnetizing** **arithmetic operations** by manipulating the single-electrons. ...

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88. @INPROCEEDINGS{0410:Quality, TITLE= "Quality Effects of wireless ...

Cost Functions: The effective cost per unit of energy for each of the proposed ... based

on a new Ternary Content Addressable Memory configuration. ...

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89. [Walker, Gordon James Conceptual Development and Empirical Testing ...](#)

The performance and the operation principles of one-choke and two-choke ... And finally, in large volumes, FPGAs tend to be more expensive per unit due to ...

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90. [\[PDF\] Effective Runtime Management of Parallelism in a Functional ...](#)

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that a processor and memory pair is connected to one or more other processor and within two neighbourhood of the central processing element). ...

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by J Demeyer · 2002 · Cited by 4 · Related articles · All 4 versions

91. [0060006 SBIR Phase I: Estimating Software Costs for Web-Enabled ...](#)

In Phase II, a film analysis unit will be mated to an existing pilot production of ternary alloy growth necessitates many more reactions and species ...

www.nsf.gov/awardsearch/search_javascript?jessessionid=2ExpDateCreator...&Search...

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92. [%T Performance Estimates for the DADO Machine: A Comparison of ...](#)

The basic unit of computation is potentially useful in checkpointing, content-addressable memory, database computer, logic-per-track, mass memory, ...

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93. [\[PDF\] AMD Geode GX1 Processor Data Book](#)

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Branch prediction logic within the prefetch unit generates a asserted followed by one or more data phases. During the address phase, ...

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processing logic of the system and when future changes in func- module calls but trampolines to jump from one compilation unit to another. ...

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97. [BIB-VERSION: CS-TR-v2.1 ID: ercim.cwi.demo/SEN-R0124 ENTRY ...](#)

Because one of the **processing units** performs a computation intensive job,
arithmetic-geometric mean difference in terms of certain **central moments**. ...

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98. [\[PDF\] GRID RESOURCE MANAGEMENT](#)

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Pegasus configured to perform the LIGO pulsar search, based on application-specific metadata. requires in each mode exactly **one resource unit** ...

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units and an accompanying engineering **unit**, under the current contract. ...

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100. [AAAI-06 Technical Paper Abstracts Tuesday, July 18](#)

When this approach is used to solve graph-search problems with **unit edge costs**, ...
show that a breadth-first search strategy can be **more memory-efficient** ...

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